

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	("20020140662" "20030016189" "6147672").PN.	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:35
L2	3330	p/s	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:00
L3	2723	s/p	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:00
L4	2925	parallel-to-serial with converter	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:01
L5	5780	I2 or I4	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:01
L6	5986	serial-to-parallel	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:01
L7	7948	I6 or I3	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:02
L8	2793	I5 and I7	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:02
L9	484712	pulse	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:02
L11	784503	display	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:02
L13	430	I8 and I9 and I11	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:03
L14	905971	transmission	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:03
L16	1448340	transfer\$4	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:03
L17	291	I13 and I14 and I16	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:04
L18	37822	shift with clock	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:04
L19	84	I17 and I18	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:04
L20	219279	latch	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:04
L21	53	I19 and I20	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:05
L22	64091	shift adj register	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:05
L23	46	I21 and I22	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:13
L24	33513	pll	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:14

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L25	2	clock with frequency with input	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:14
L26	24117	clock with frequency with input	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:14
L27	2735	I24 same I26	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:14
L28	35	I27 and I8	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:15
L29	10	("5598156" "5721755" "5790058" "5808571" "5818365" "6018305" "6069927" "6266383" "6300982" "6452591").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/09 13:21
L30	268	clock adj conversion	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:35
L31	4659	clock with conversion with circuit	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:35
L32	37729	fifo	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:36
L33	851542	memory	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:36
L34	23	I31 same I32 same I33	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:36
L35	959846	frequency	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:36
L36	16	I35 and I34	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:40
L37	83146	control with (data or signal) with display	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:42
L38	243	fifo same clock same frequency same written same read	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:43
L39	35	I37 and I38	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:53
L40	65	tetsuya with shigeta	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:53
L41	52	tetsuro with nagakubo	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:54
L42	3	kazuhisa with ata	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:54
L43	81	I40 or I41 or I42	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:55
L44	2	I43 and I8	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:56
L45	2	I43 and I32	US-PGPUB; USPAT	OR	OFF	2006/05/09 13:59

EAST Search History

L46	2	(parallel-to-serial and display and conver\$4 and transmission and serial-to-parallel and pulse and transfer and reception and shift and clock and latch and register and address).clm.	US-PGPUB	OR	OFF	2006/05/09 14:04
L49	1	(parallel-to-serial and display and conver\$4 and transmitter\$1 and pll and first and second and frequency and serial-to-parallel and pulse and transfer\$4 and reception and clock and synchronism and differential).clm.	US-PGPUB	OR	OFF	2006/05/09 14:11